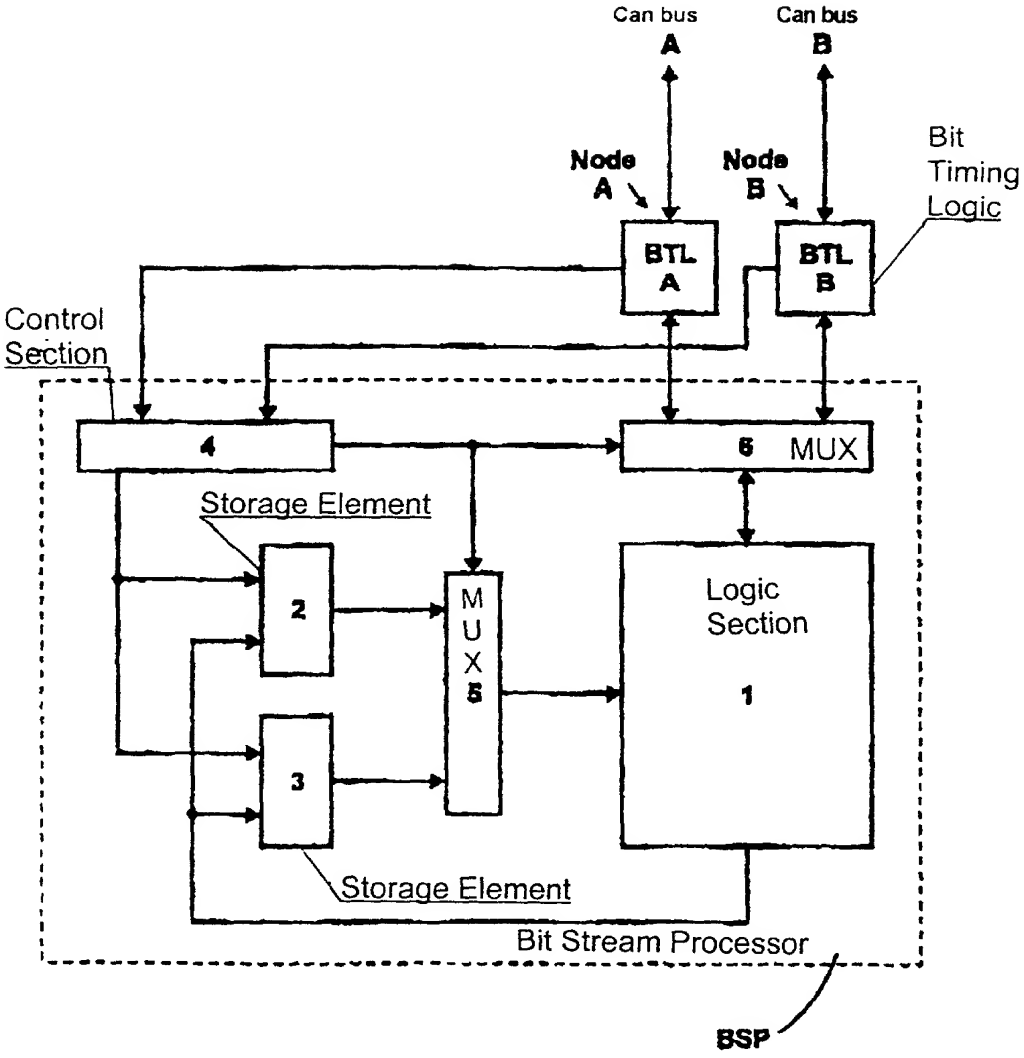
Can bus
A
Can bus
B
Node
A
Node
B
Bit
Timing
Logic
BTL
A
BTL
B
Control
Section
4
6
MUX
Storage Element
2
M
U
X
5
Logic
Section
1
3
Storage Element
Bit Stream Processor
BSP